

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) A method for connecting signals between a radio integrated circuit (IC) and a signal processing device comprising the step of multiplexing two or more signals on a connection pin between the radio IC and the signal processing device;

wherein in a transmit mode, the step of multiplexing comprises multiplexing a transmit in-phase (I) signal and a transmit quadrature (Q) signal on a connection pin between the radio IC and the signal processing device; and wherein in a receive mode, the step of multiplexing comprises multiplexing a receive in-phase (I) signal and a receive quadrature (Q) signal on a connection pin between the radio IC and the signal processing device.

2. (Original) The method of claim 1, wherein the step of multiplexing comprises multiplexing transmit and receive signals on the connection pin such that during a transmit mode a transmit signal is coupled on the connection pin from the signal processing device to the radio IC, and during a receive mode a receive signal is coupled from the radio IC on the connection pin to the signal processing device.

3. (Original) The method of claim 2, wherein the step of multiplexing is performed on each of a plurality of connection pins between the radio IC and the signal processing device for a plurality of corresponding transmit and receive signals.

4. (Canceled)

5. (Original) The method of claim 2, wherein the step of multiplexing is performed on each of a pair of connection pins associated with differential transmit signals and differential receive signals.

6-17. (Canceled)

18. (Original) A radio transceiver integrated circuit (IC) comprising:

- a. at least one transmitter and at least one receiver, each of which includes an in-phase (I) signal path and a quadrature (Q) signal path;
- b. a transmitter connection pin on which a transmit I signal and a transmit Q signal are multiplexed for connection to the respective I and Q signal paths of the transmitter; and
- c. a receiver connection pin on which a receive I signal and a receive Q signal are multiplexed from the respective I and Q signal paths of the receiver.

19. (Original) The radio IC of claim 18, and further comprising a first multiplexer circuit that couples from the transmitter connection pin the transmit I and transmit Q signals during different time intervals for connection to the I and Q signal paths, respectively, of the transmitter.

20. (Original) The radio IC of claim 19, wherein the first multiplexer circuit alternately couples the transmit I and transmit Q signals, respectively, from the transmitter pin to the I and Q signal paths, respectively, of the transmitter.

21. (Original) The radio IC of claim 19, and further comprising a second multiplexer circuit that couples during different time intervals the receive I and

receive Q signals to the receiver connection pin from the I and Q signal paths, respectively, of the receiver.

22. (Original) The radio IC of claim 21, wherein the second multiplexer circuit alternately couples the receive I and receive Q signals to the receiver connection pin from the I and Q paths, respectively, of the receiver.

23. (Original) The radio IC of claim 18, and further comprising a multiplexer circuit that during a receive mode couples to the receiver connection pin the receive I and receive Q signals during different time intervals from the I and Q signal paths, respectively, of the receiver, and during a transmit mode, couples from the transmitter connection pin the transmit I and transmit Q signals during different time intervals to the I and Q signal paths, respectively of the transmitter.

24. (Original) The radio IC of claim 23, and further comprising a timing circuit that supplies a timing signal to the multiplexer circuit to control the timing of the first and second multiplexer circuits.

25. (Original) The radio IC of claim 18, and further comprising a plurality of transmitters and a plurality of receivers such that a pair of a transmitter and a receiver are associated with a corresponding one of a plurality of antennas connected to the radio IC, and further comprising a plurality of transmitter connection pins each associated with a corresponding transmitter and a plurality of receiver connection pins each associated with a corresponding receiver, each transmitter connection pin on which corresponding transmit I and transmit Q signals are multiplexed for connection to the I and Q signal paths, respectively, of

the associated transmitter and each receiver connection pin on which corresponding receive I and receive Q signals are multiplexed for connection from the I and Q signal paths, respectively, of the associated receiver.

26. (Original) The radio IC of claim 25, and further comprising a plurality of transmitter multiplexer circuits each associated with a corresponding transmitter to multiplex the corresponding transmit I signal and transmit Q signal from the corresponding transmitter connection pin to the respective I and Q signal paths of the corresponding transmitter.

27. (Original) The radio IC of claim 26, wherein each transmitter multiplexer circuit alternately couples the corresponding transmit I signal and transmit Q signal from the corresponding transmitter connection pin to the respective I and Q signal paths of the corresponding transmitter.

28. (Original) The radio IC of claim 25, and further comprising a plurality of receiver multiplexer circuits each associated with a corresponding receiver to multiplex the corresponding receive I signal and receive Q signal from the respective signal paths of the corresponding receiver to the corresponding receiver connection pin.

29. (Original) The radio IC of claim 28, wherein each receiver multiplexer circuit alternately couples the corresponding receive I and receive Q signal to the corresponding receiver connection pin from the respective I and Q signal paths of the corresponding receiver.

30. (Original) The radio IC of claim 25, and further comprising a plurality of multiplexer circuits each associated with a corresponding transmitter/receiver pair that multiplexes the corresponding transmit I signal and transmit Q signal from the corresponding transmitter connection pin to the respective I and Q signal paths of the corresponding transmitter, and that multiplexes the corresponding receive I signal and receive Q signal from the respective I and Q signal paths of the corresponding receiver to the corresponding receiver connection pin.

31. (Original) The radio IC of claim 25, wherein each of the I and Q signal paths of each the of transmitters and receivers having differential signal paths, and further comprising a plurality of pairs of transmitter connection pins on which corresponding differential transmit I and transmit Q signals are multiplexed, and a plurality of pairs of receiver connection pins on which corresponding differential receive I and receive Q signals are multiplexed.

32. (Original) The radio IC of claim 18, wherein each of the I and Q signal paths of the transmitter and receiver are differential, and further comprising a pair of transmitter connection pins on which differential transmit I and transmit Q signals are multiplexed, and a pair of receiver connection pins on which differential receive I and receive Q signals are multiplexed.

33. (Original) The radio IC of claim 32, and further comprising a plurality of transmitters and a plurality of receivers such that a pair of a transmitter and a receiver are associated with a corresponding one of a plurality of antennas connected to the radio IC, and further comprising a plurality of pairs of transmitter connection pins each pair associated with a corresponding transmitter and a

plurality of pairs of receiver connection pins each pair associated with a corresponding receiver, each pair of transmitter connection pins on which corresponding differential transmit I and transmit Q signals are multiplexed to the I and Q signal paths, respectively, of the associated transmitter and each pair of receiver connection pin on which corresponding differential receive I and receive Q signals are multiplexed from the I and Q signal paths, respectively, of the associated receiver.

34. (Original) A system comprising the radio IC of claim 18, and further comprising a signal processing device coupled to the radio IC that supplies the transmit I signal and transmit Q signal to the radio IC for transmission processing and processes the receive I signal and receive Q signal from the radio IC.

35. (Original) The system of claim 34, and further comprising a timing circuit in the radio IC and a timing circuit in the signal processing device, wherein the timing circuit in the radio transceiver or timing circuit in the signal processing device generates timing signals that are coupled to the other timing circuit in order to coordinate the transfer of transmit I and transmit Q signals from the signal processing device to the radio IC and to coordinate transfer of the receive I and receive Q signals from the radio IC to the signal processing device.

36. (Original) The system of claim 35, wherein the radio IC further comprises at least one multiplexer circuit coupled to the timing circuit in the radio IC and is responsive to timing signals supplied by the timing circuit to couple from the transmitter connection pin the transmit I and transmit Q signals during different time intervals for connection to the I and Q signal paths, respectively, of the

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transmitter, and to couple to the receiver connection pin the receive I and receive Q signals during different time intervals from the I and Q signal paths, respectively, of the receiver.

37 -38. (Canceled)